



## GPIO


[illegible]

Supply order	Supply	Default voltage	I <sub>max</sub>	Default Enable	Usage
0	Buck1	1.15V	3000	On	V <sub>CC</sub> _MAIN, G <sub>2</sub> _V <sub>DD</sub> _1F0
1	LDO18	1.8V	200	On	V <sub>LDO</sub> _RF_1V8, V <sub>DDIC3</sub> _PMB12
2	Buck2	1.8V	1200	On	1.8V I/O, NAND LPDDF2(V <sub>DD1</sub> ), LCD/TP I/O
3	Buck3	1.2V	1200	On	RF 1.2V, HSIC, LPDDF2(V <sub>DD2</sub> /V <sub>DDQ</sub> /V <sub>DDCA</sub> ), V <sub>DD</sub> _CODEC
4	LDO1 LV	1.1V	200	On	G <sub>2</sub> _V <sub>DD</sub> _1F0 (Option reserved)
4	LDO3	2.8V	300	On	V <sub>LDO</sub> _RF_2V8
5	Buck4	1.8V	1200	On	V <sub>BUCK</sub> _RF_1V8
6	LDO7	2.8V	300	On	TBD
6	LDO5	3.1V	300	On	AV <sub>DD</sub> _USB
7	LDO14	2.8V	300	On	V <sub>CC</sub> _EMMC
0	Buck5	1.1V	1200	Off	RF APT
0	LDO6	2.8V	300	Off	WIB_3V3
0	LDO8	2.8V	300	Off	V <sub>CC</sub> _LCD_2V8, V <sub>DD</sub> _TP_2V8
0	LDO11	2.8V	300	Off	TBD
0	LDO9	1.8V	300	Off	WIB_1V8
0	LDO2 (I <sub>max</sub> 10 mA)	2.0V	10	Off	MICBIAS_EXT
0	LDO 4	2.8V	300	Off	USIM1
0	LDO10	2.8V	300	Off	USIM2
0	LDO12	2.8V	300	Off	V <sub>CC</sub> _IO_MMC1
0	LDO13	2.8V	300	Off	V <sub>CC</sub> _SDCARD
0	LDO15	2.8V	300	Off	TBD
0	LDO16	2.8V	300	Off	TBD
0	LDO17	2.8V	300	Off	TBD
0	LDO19	2.8V	200	Off	V_GPS_1V8

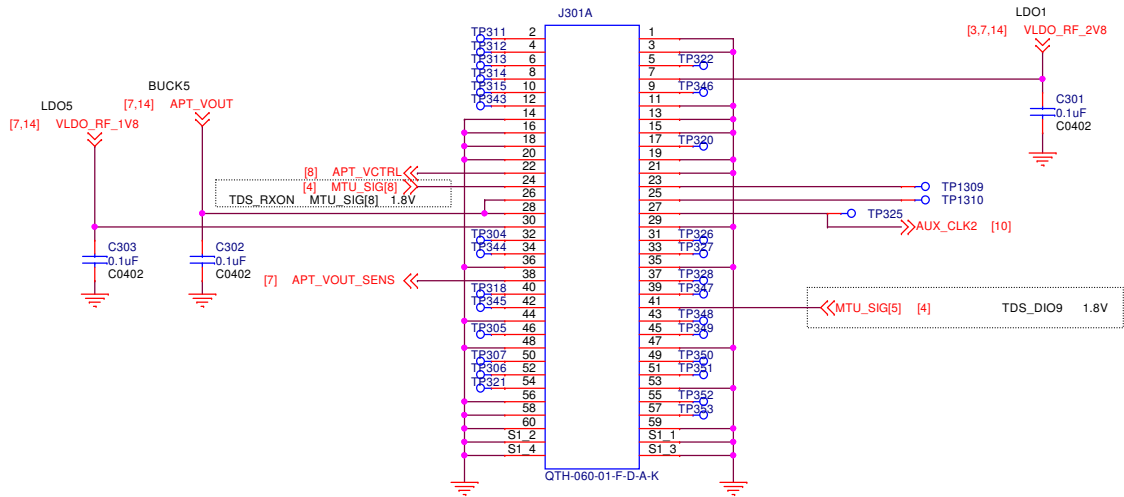
The diagram illustrates the clock system architecture. It features three main components: Argus, Nezha3, and 88PM812. Argus is connected to Nezha3 via two signals: SYS\_CLK and SYS\_CLK\_EN. Argus is also connected to a 26M VCTCXO. Nezha3 is connected to 88PM812 via a 32KHz signal. 88PM812 is connected to a 32K XTAL.

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graph TD; Argus[Argus] -- SYS_CLK --> Nezha3[Nezha3]; Argus -- SYS_CLK_EN --> Nezha3; Argus --- VCTCXO[26M VCTCXO]; Nezha3 -- 32KHz --> 88PM812[88PM812]; 88PM812 --- XTAL[32K XTAL];
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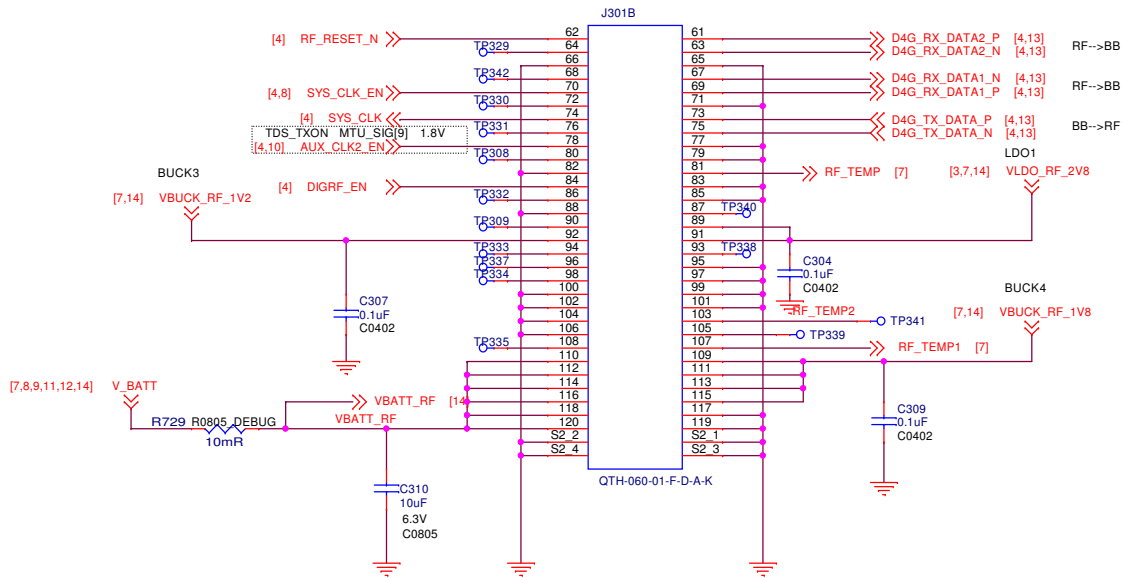
Device	Pt2C (W/R)		Ct2C (W/R)
PMB12	0x60/0x61,0x62/0x63,0x64/0x65,0x66/0x67,0x68/0x69,0x6A/0x6B,0x6C/0x6D,0x6E/0x6F	CTP	0XB8/0XB9
BQ24193/BQ24192	0XD6/0XD7	OLED DISPLAY	0x7A/0x7B
PMB30	0XD1/0XD2		

Designer	PHS HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	DOC NO.	Rev	
A2	RD-PHS-HW-SCHxxxxxx	1.0	
	PCB NO.	LAST UPDATE	
	PCBxxxxxx	Monday, February 02, 2015	
	<b>Confidential</b>	Sheet 2 of 14	

RF CONNECTOR

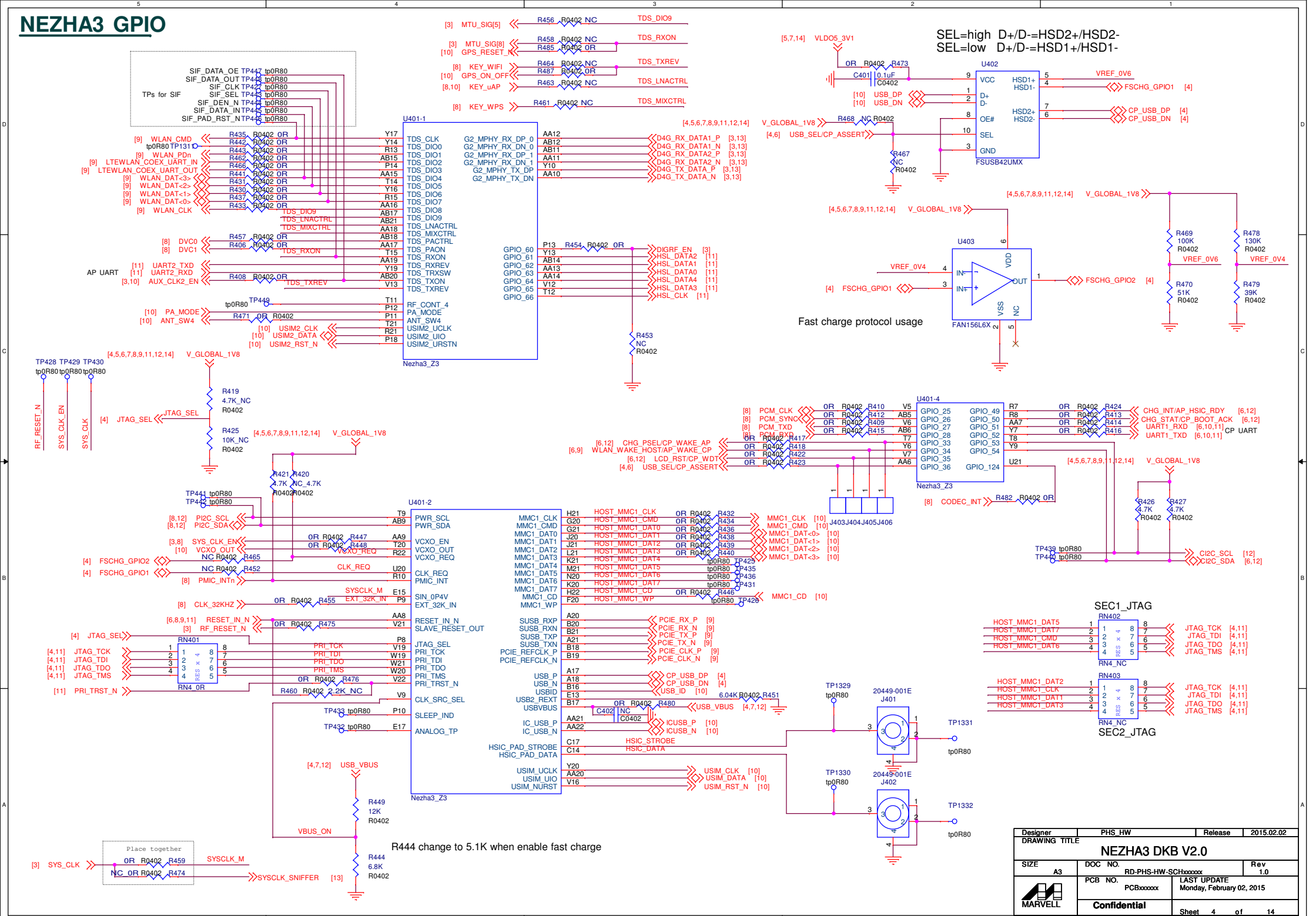


Dot box note:  
MTU GPIOs reserved for CANARY RF CARD

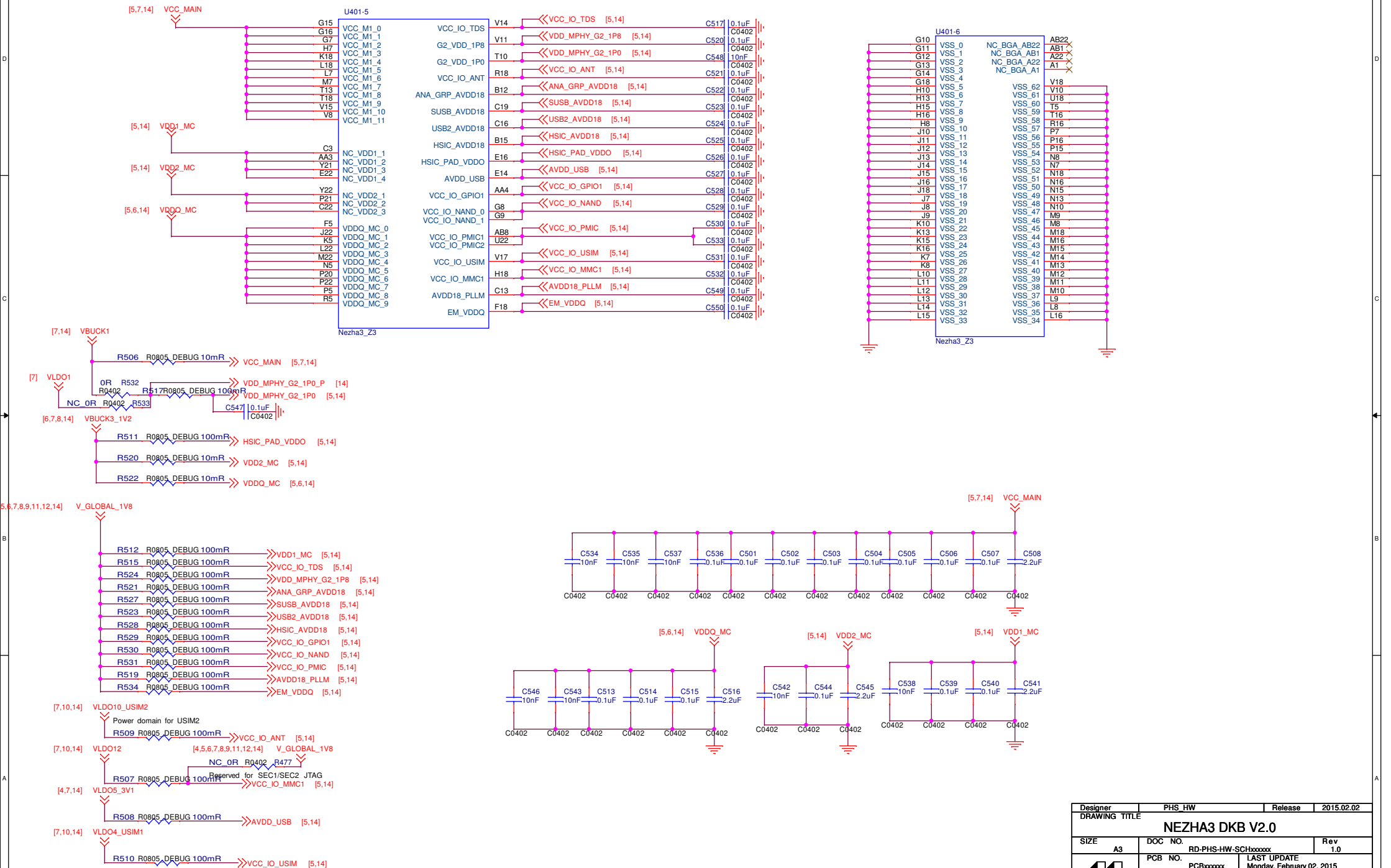



Designer	PHS_HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	DOC NO.	Rev	
A3	RD-PHS-HW-SCHxxxxx	1.0	
MARVELL	PCB NO.	LAST UPDATE	
	PCBxxxxx	Monday, February 02, 2015	
Confidential		Sheet	3 of 14

## NEZHA3 GPIO

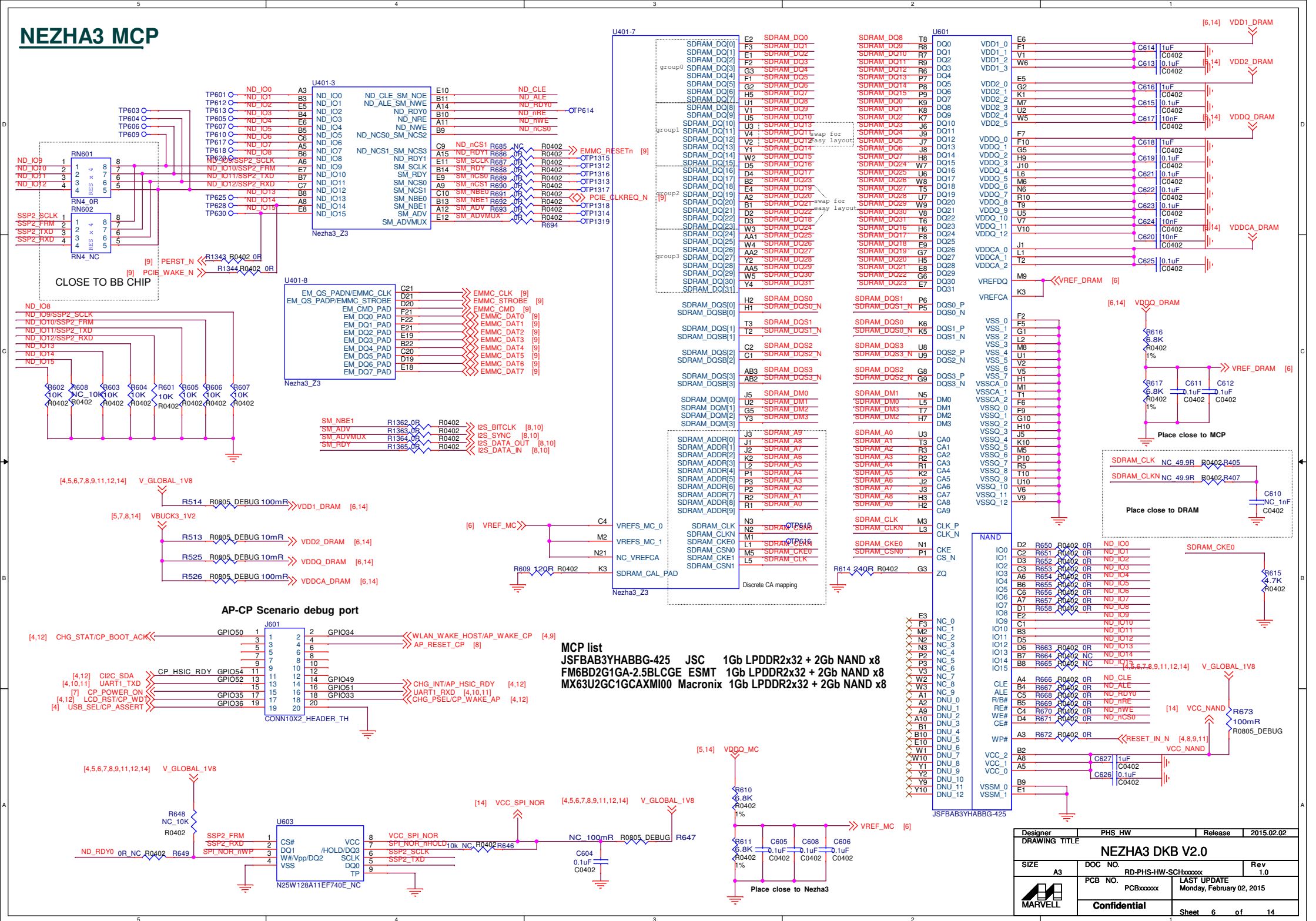


## NEZHA3 POWER DOMAIN



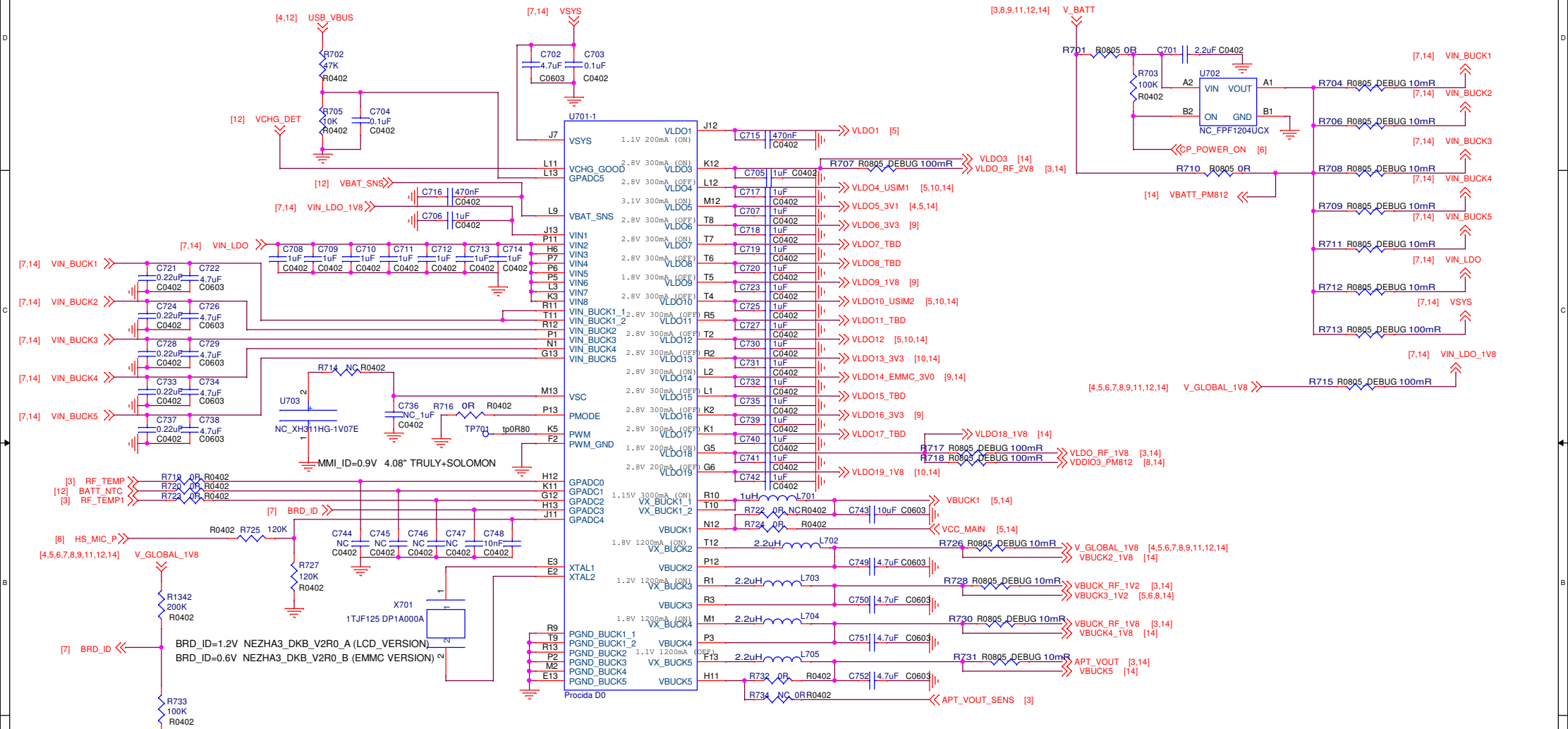
Designer	PHS HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	DOC NO.		Rev
A3	RD-PHS-HW-SCHxxxxxx		1.0
	PCB NO.	LAST UPDATE	
	PCBxxxxxx	Monday, February 02, 2015	
	<b>Confidential</b>	Sheet	5 of 14

# NEZHA3 MCP





PM812 POWER SUPPLY



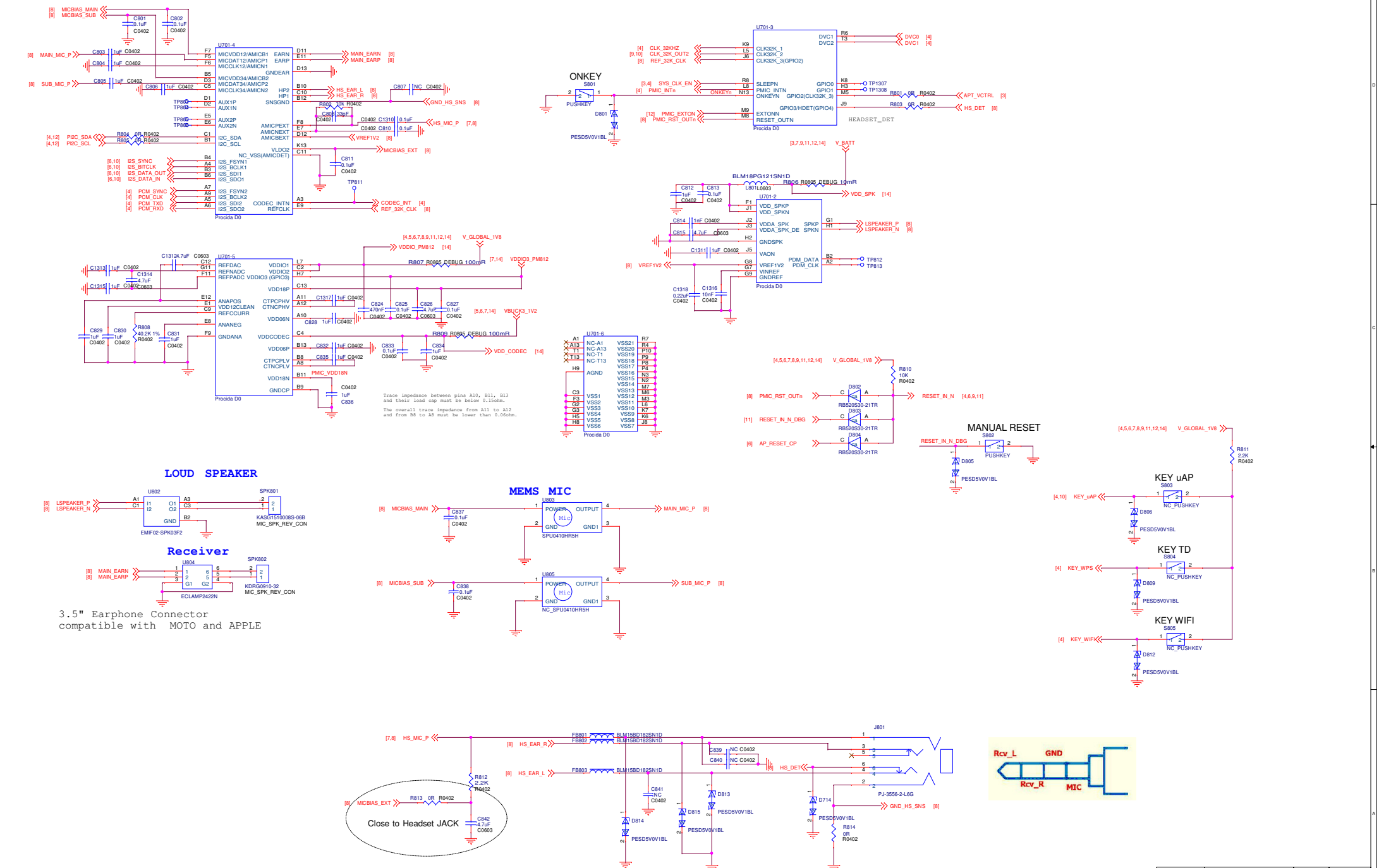
Procida D0

I2C address:0x60/0x61,0x62/0x63,0x64/0x65,0x66/0x67,0x68/0x69,0x6A/0x6B,0x6C/0x6D,0x6E/0x6F,0x70/0x71

PLATFORM MODE: 0x0

Designer	PHS_HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	A3	DOC NO.	RD-PHS-HW-SCHxxxxx
PCB NO.	PCBxxxxxx	LAST UPDATE	Monday, February 02, 2015
MARVELL		Confidential	Sheet 7 of 14

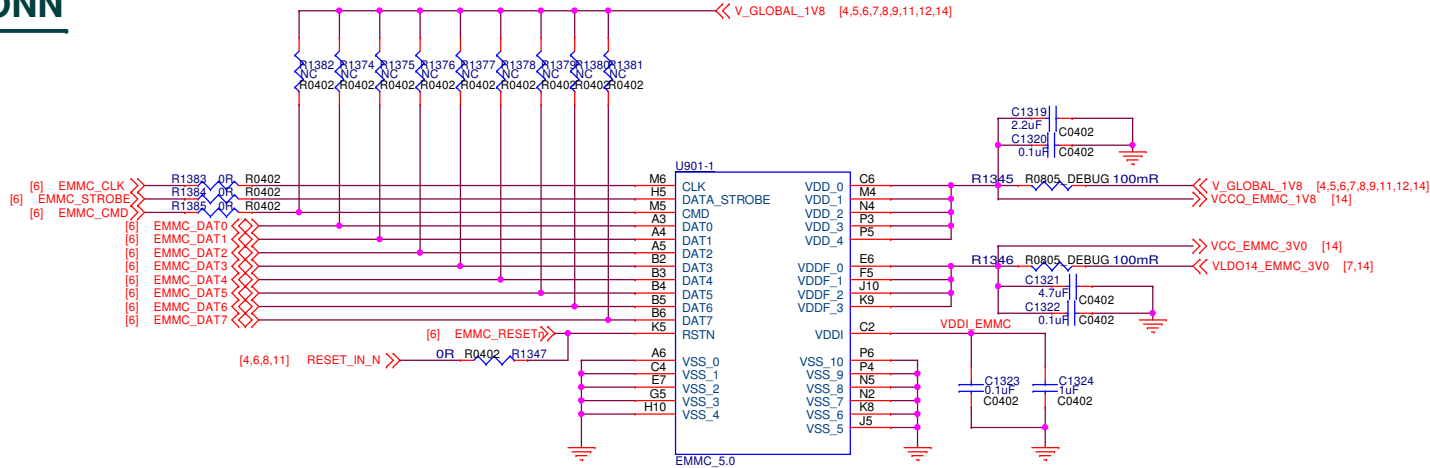
PM812 MSIC/AUDIO CODEC



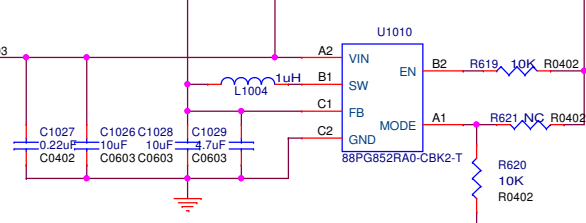
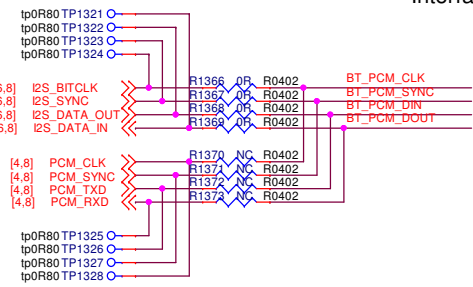
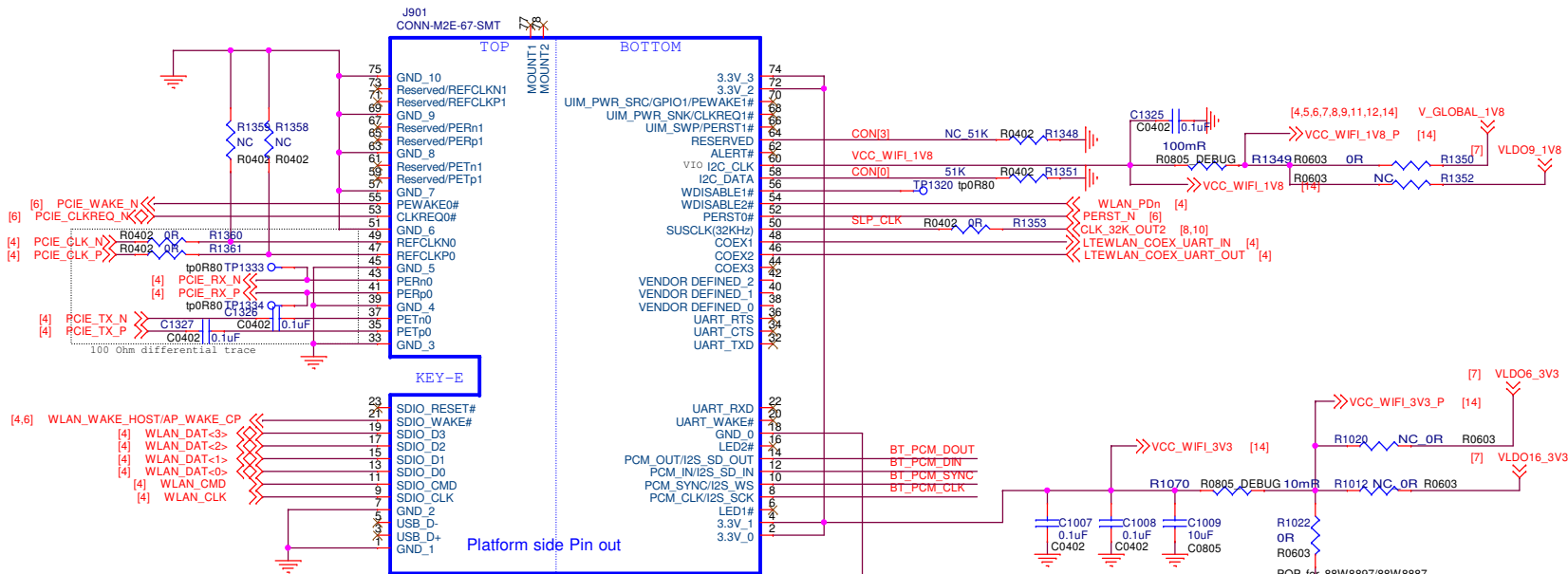
DESIGNER	PHS_HW	Release	2015.02.02
DRAWING TITLE	NEZHA3 DKB V2.0		
SIZE	DOC NO.	Rev	1.0
A2	RD-PHS-HW-Schxxxx	LAST UPDATE	Monday, February 02, 2015
PCB NO.	PCBxxxx	CONFIDENTIAL	
CONFIDENTIAL	Sheet 8 of 14		



EMMC/NGF CONN

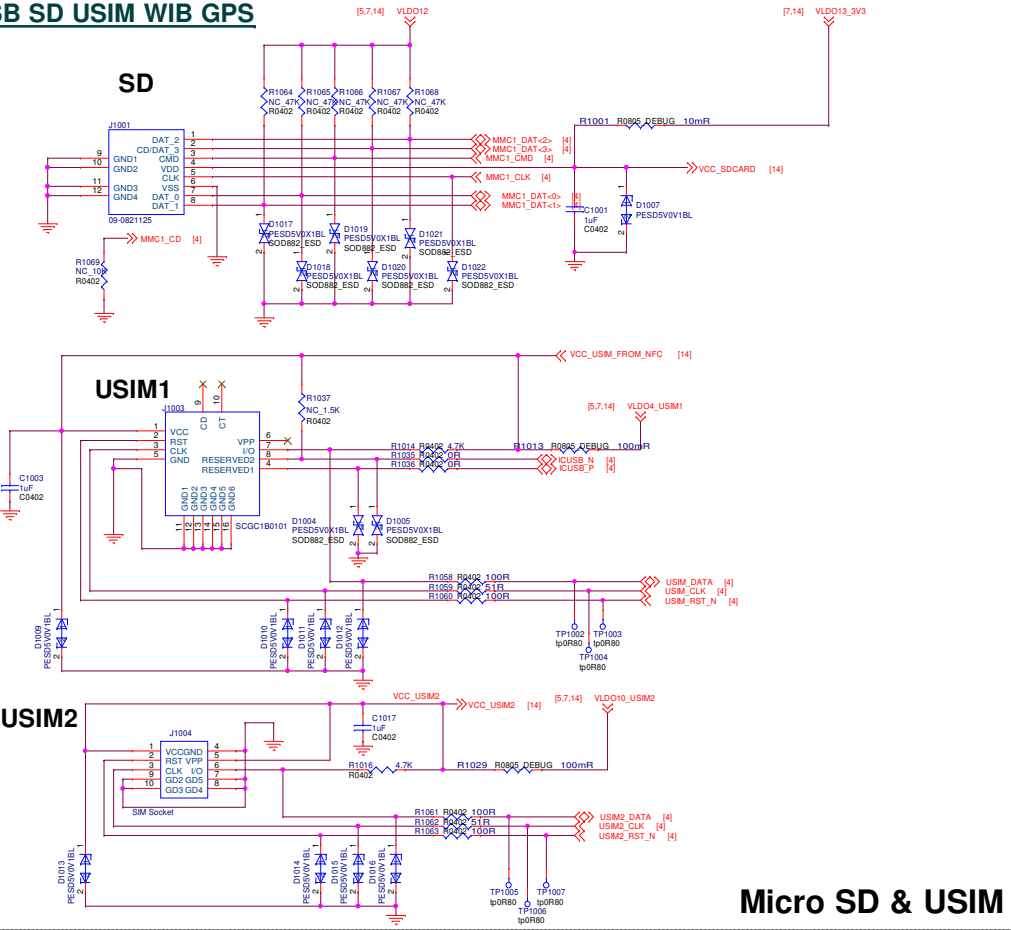


U901-2			
A1	NC_0	NC_119	P13
A2	NC_1	NC_118	P12
A7	NC_2	NC_117	P11
A8	NC_3	NC_116	P10
A9	NC_4	NC_115	P9
A10	NC_5	NC_114	P8
A11	NC_6	NC_113	P7
A12	NC_7	NC_112	P6
A13	NC_8	NC_111	P5
A14	NC_9	NC_110	P4
B1	NC_10	NC_109	P3
B7	NC_11	NC_108	P2
B8	NC_12	NC_107	P1
B9	NC_13	NC_106	
B10	NC_14	NC_105	
B11	NC_15	NC_104	
B12	NC_16	NC_103	
B13	NC_17	NC_102	
B14	NC_18	NC_101	
C1	NC_19	NC_100	
C3	NC_20	NC_99	
C7	NC_21	NC_98	
C8	NC_22	NC_97	
C9	NC_23	NC_96	
C10	NC_24	NC_95	
C11	NC_25	NC_94	
C12	NC_26	NC_93	
C13	NC_27	NC_92	
C14	NC_28	NC_91	
D1	NC_29	NC_90	
D2	NC_30	NC_89	
D3	NC_31	NC_88	
D4	NC_32	NC_87	
D12	NC_33	NC_86	
D13	NC_34	NC_85	
D14	NC_35	NC_84	
E1	NC_36	NC_83	
E2	NC_37	NC_82	
E3	NC_38	NC_81	
E5	NC_39	NC_80	
E8	NC_40	NC_79	
E9	NC_41	NC_78	
E10	NC_42	NC_77	
E12	NC_43	NC_76	
E13	NC_44	NC_75	
E14	NC_45	NC_74	
F1	NC_46	NC_73	
F2	NC_47	NC_72	
F3	NC_48	NC_71	
F10	NC_49	NC_70	
F12	NC_50	NC_69	
F13	NC_51	NC_68	
F14	NC_52	NC_67	
G1	NC_53	NC_66	
G2	NC_54	NC_65	
G3	NC_55	NC_64	
G10	NC_56	NC_63	
G12	NC_57	NC_62	
G13	NC_58	NC_61	
	NC_59	NC_60	



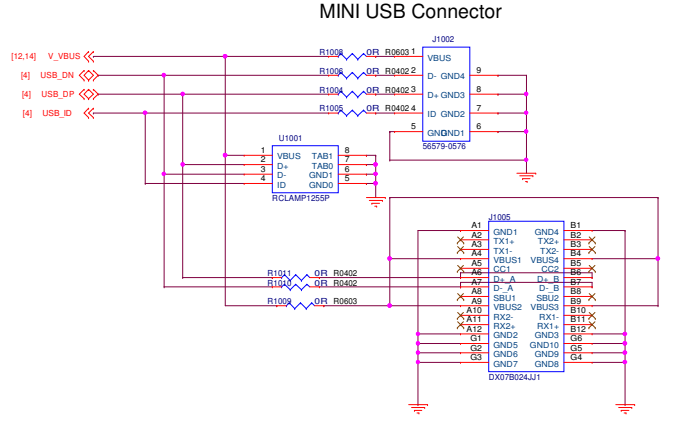
Designer	PHS_HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	DOC NO.	Rev	1.0
A3	RD-PHS-HW-SCHxxxxx	PCB NO.	PCBxxxxxx
MARVELL		LAST UPDATE	
Confidential		Monday, February 02, 2015	
Sheet		9 of 14	

USB SD USIM WIB GPS

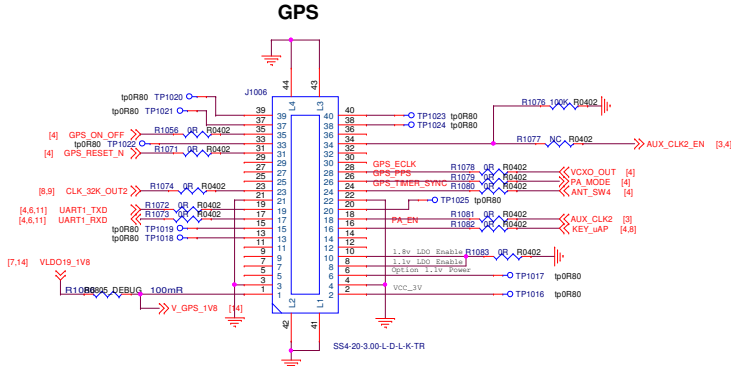


Micro SD & USIM

USB Connector



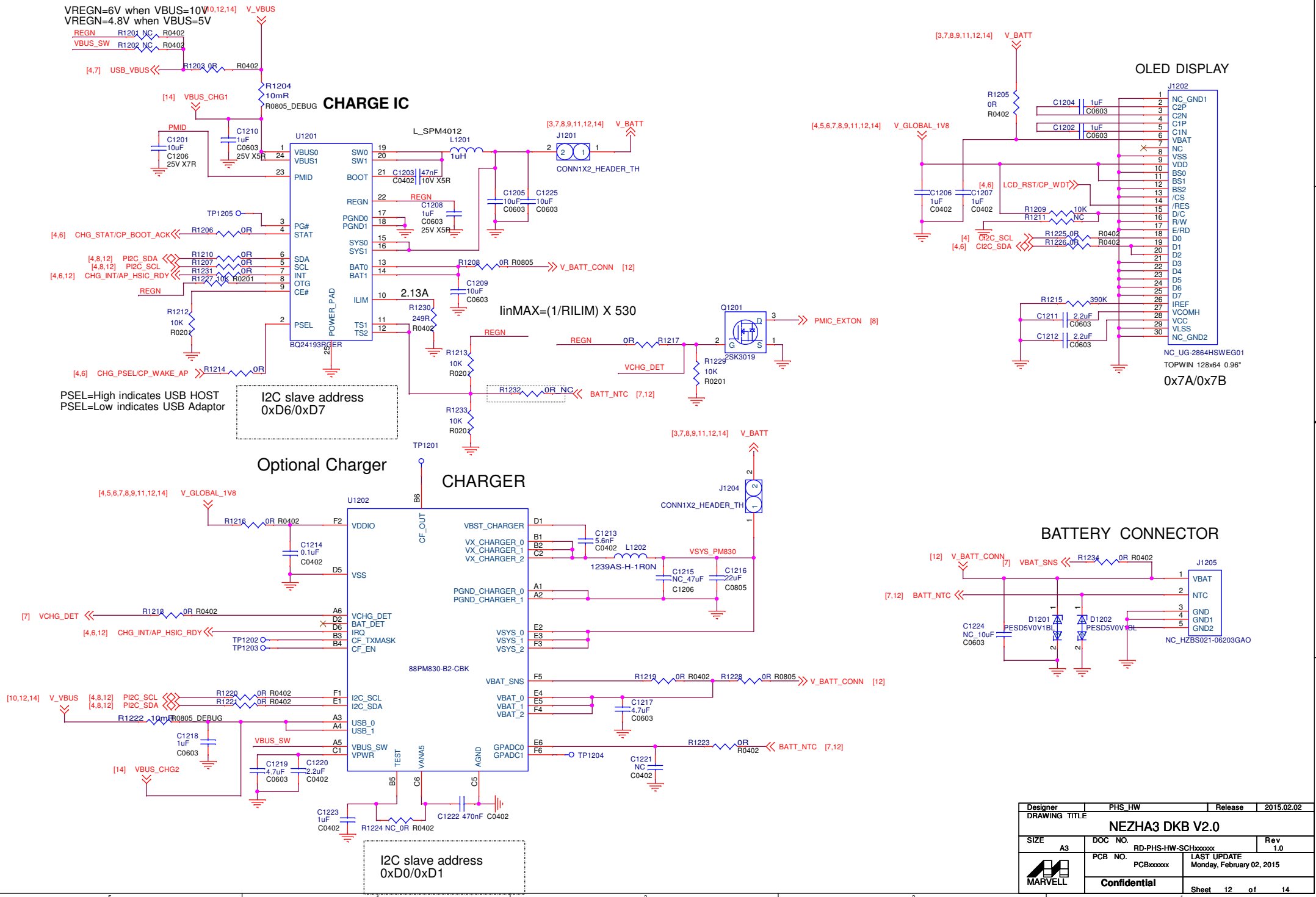
GPS Connector



DESIGN	PHS_HW	Release	2015.02.02
DRAWING	TITLE		
	NEZHA3 DKB V2.0		
SIZE	DOC NO.	Rev	
A2	RQ-PHS-HW-Schxxxx	1.0	
PCB NO.	PCBxxxxx	LAST UPDATE	
	Monday, February 02, 2015		
Confidential		Sheet	10 of 14

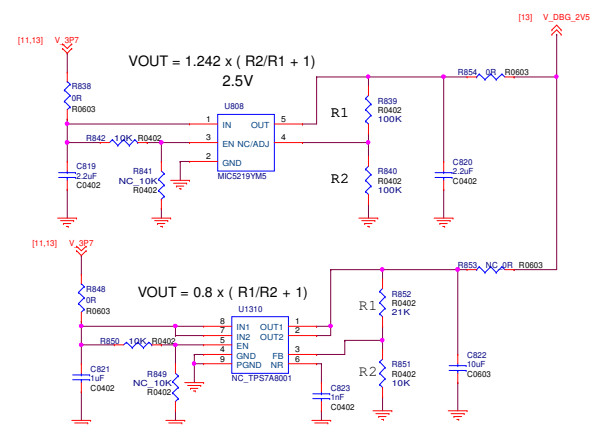
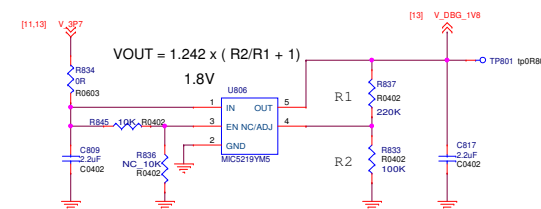
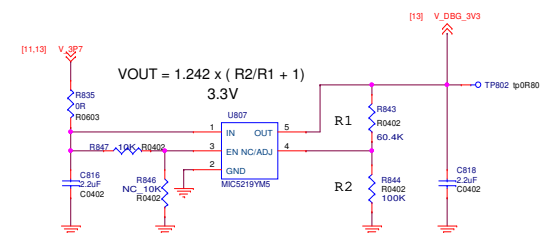
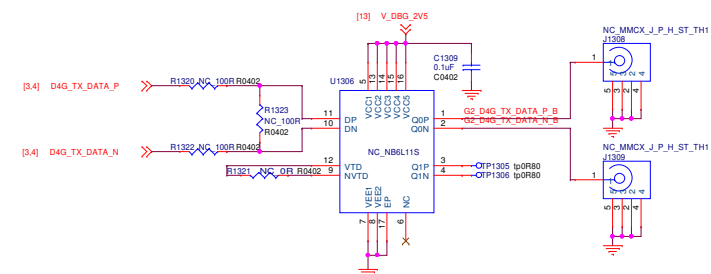
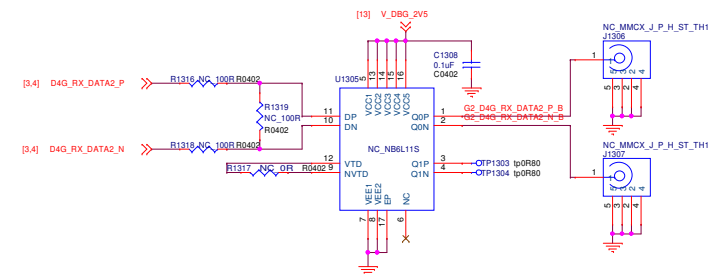
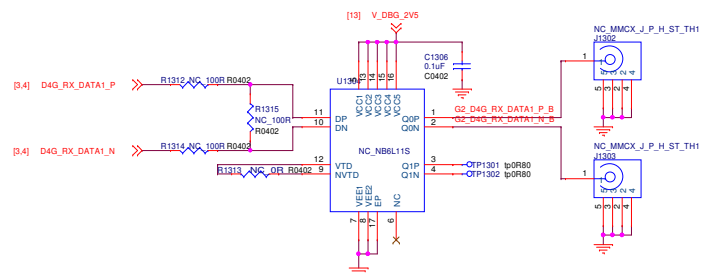
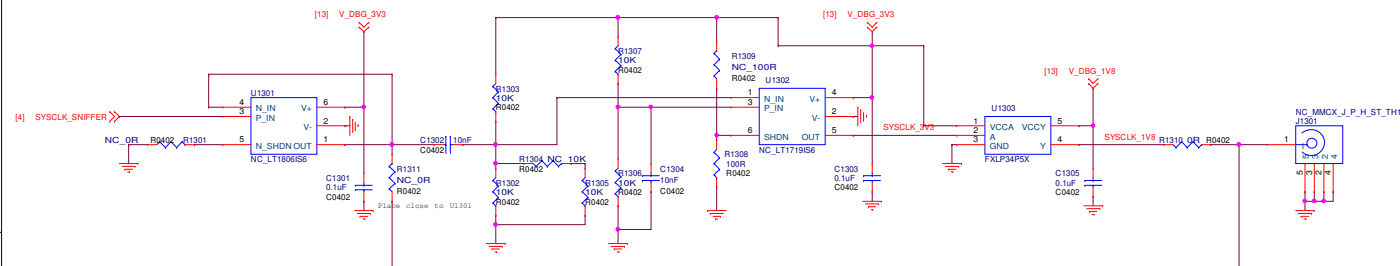



# CHARGER/OLED DISPLAY

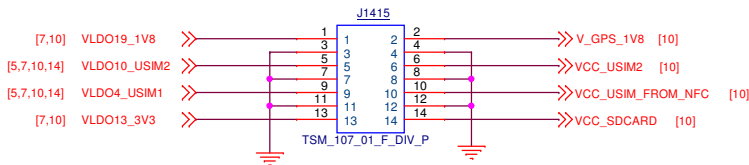
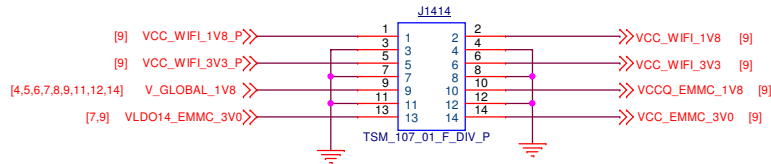
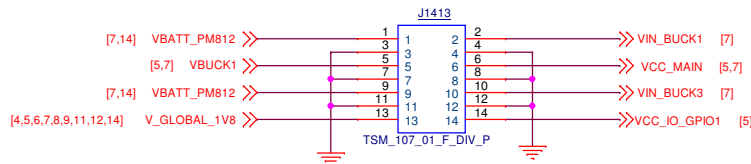
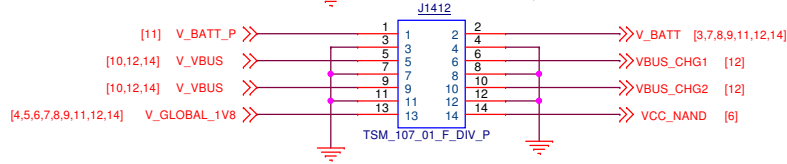
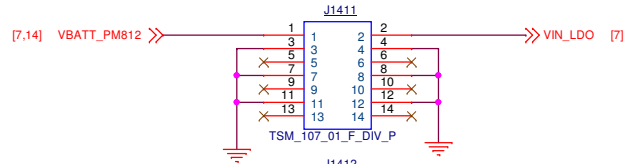
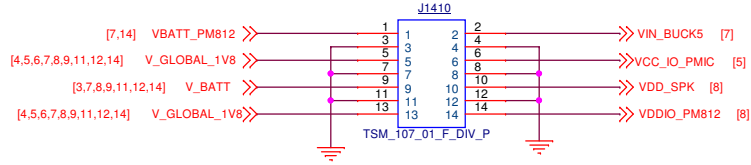
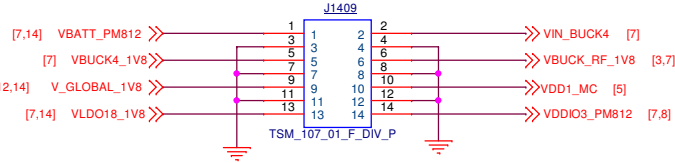
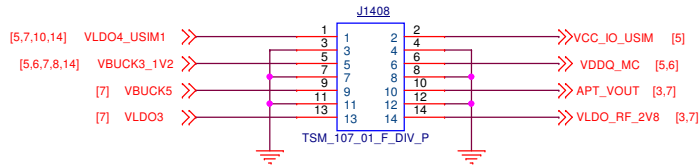
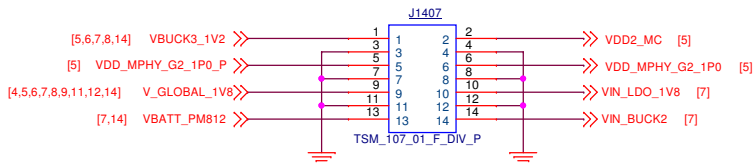
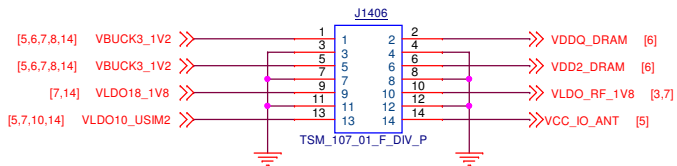
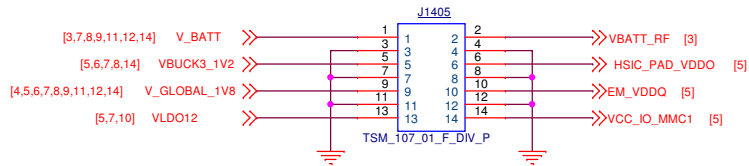
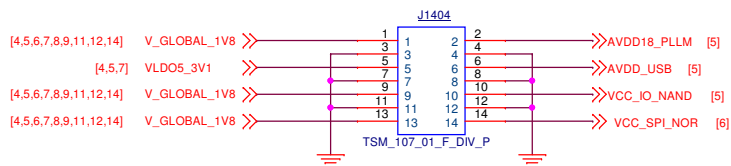
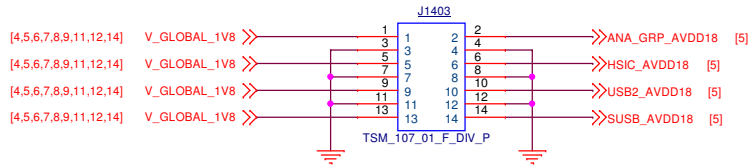
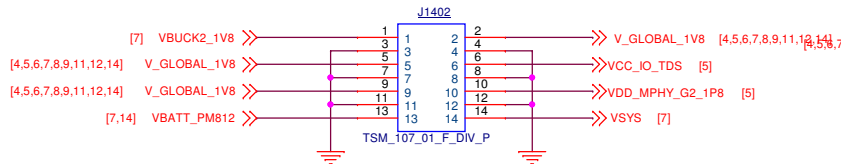
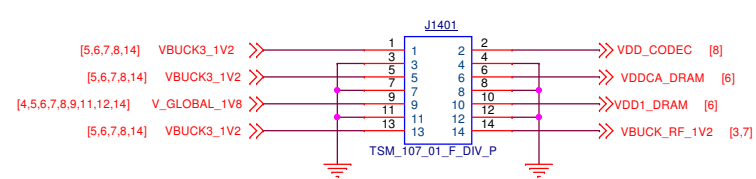


Designer	PHS_HW	Release	2015.02.02
DRAWING TITLE	NEZHA3 DKB V2.0		
SIZE	A3	DOC NO.	RD-PHS-HW-SCHxxxxxx
PCB NO.	PCBxxxxxx	LAST UPDATE	Monday, February 02, 2015
MARVELL		Confidential	Sheet 12 of 14

## DIGRF4G SNIFFER

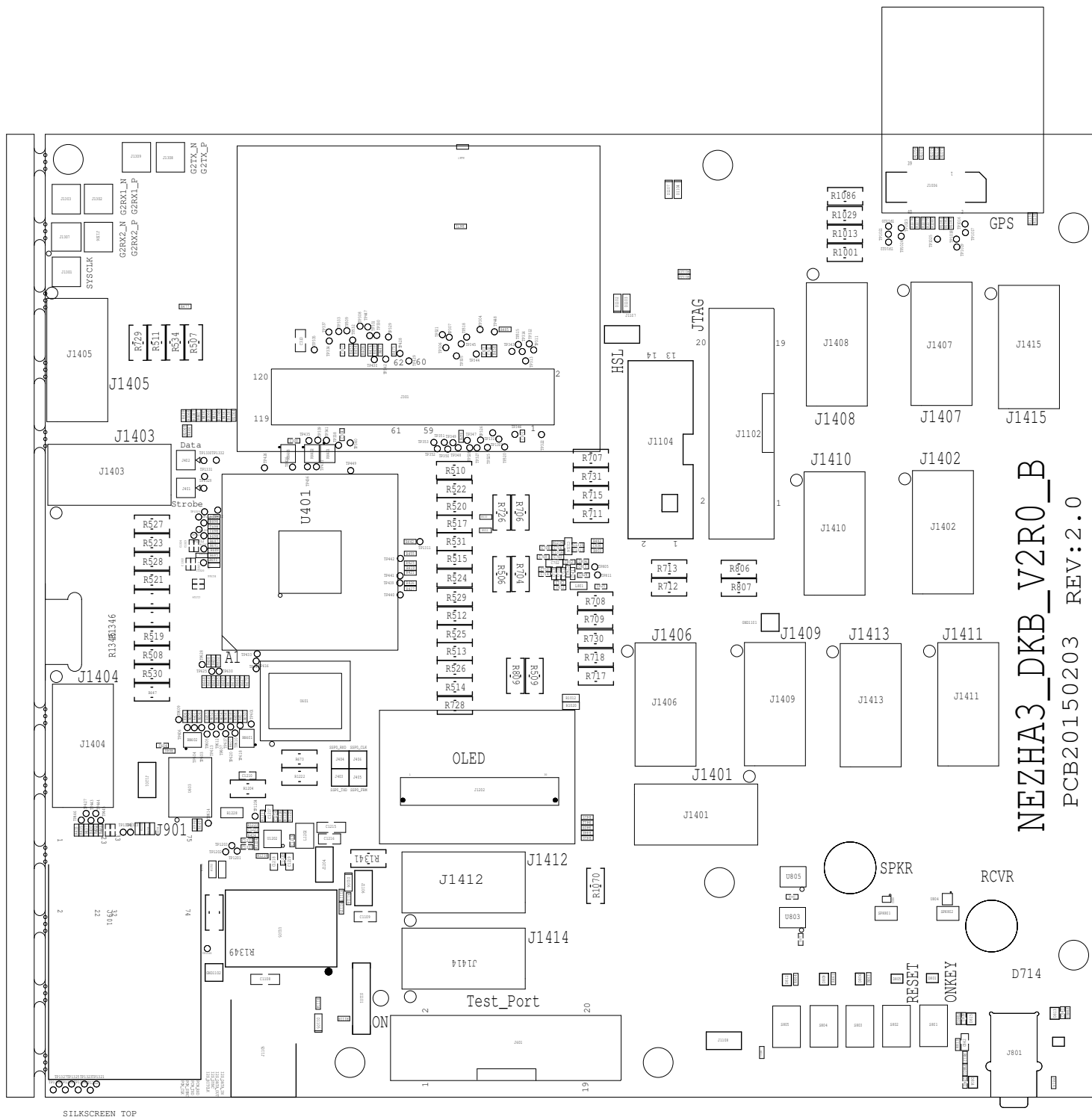


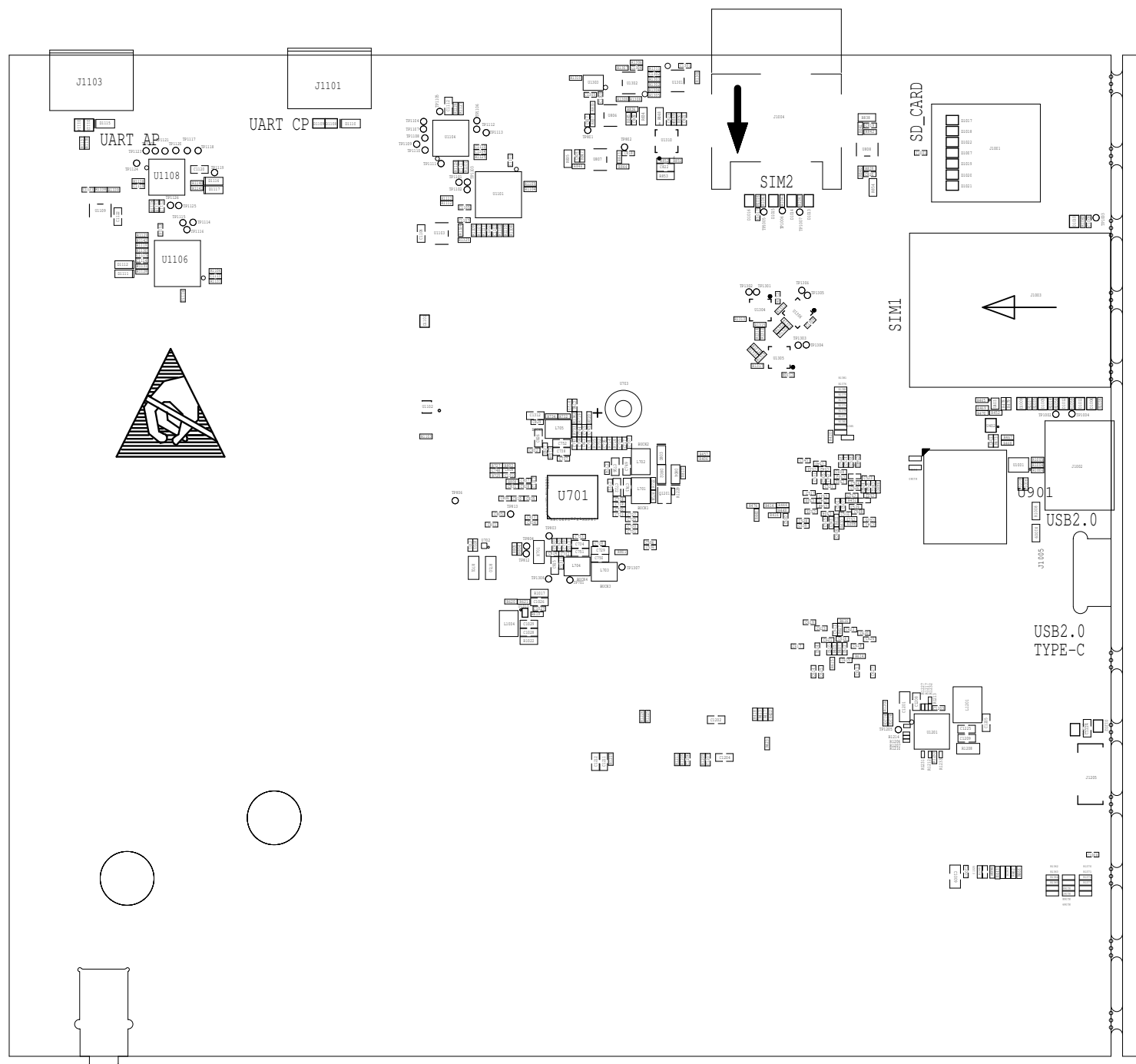
Designer	PHS HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	DOC NO.	Rev	
A2	RD-PHS-HW-SCHXXXXXX	1.0	
	PCB NO.	LAST UPDATE	
	PCBXXXXXX	Monday, February 02, 2015	
	<b>Confidential</b>		
Sheet		13	of 14



Designer	PHS_HW	Release	2015.02.02
DRAWING TITLE			
NEZHA3 DKB V2.0			
SIZE	DOC NO.	Rev	
A3	RD-PHS-HW-SCHxxxxxx	1.0	
PCB NO.	PCBxxxxxx	LAST UPDATE	
Confidential		Monday, February 02, 2015	
MARVELL		Sheet 14 of 14	







SILKSCREEN BOTTOM